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# SPECIFICATIONS 产品规格书

MODULE NO.(产品型号): PV08009H0130J

Customer Name: (客户名称)	
Customer P/N: (客户型号)	
Data: (日期)	2022-02-24
Version: (版本)	V1. 0
Custo	mer Approval(客户承认)

Prepare(制作)	Check(审核)	Approval(核准)



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# Revision History(修订历史)

REV	REVISED DESCRIPTIONS (修订描述)	DATE (日期)
V1.0	Generation first version	2022-02-24





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# 1 LCD MODULE PHYSICAL DATA

# 1.1 **Features**

Display Type	TFT
Viewing Direction	ALL
Connection Type	COG + FPC+BL+LENS
Driving IC	FT8201
MPU interface	MIPI
Backlight	LB:4S6P= 24LEDs

Table 1.

# 1.2 Mechanical Specification

Item	Standard Value	Unit
Screen size	8.0	inch
Number of dots	800RGB x 1280 dots	pixel
Boundary dimension	121.50(H ) x207.60(V) x4.61(T)	mm
Active area	0.13455H) x0.13455(V)	mm
Dot size		um
Approx. weight	TBD	g

Table 2.



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# **2 OUTLINE DIMENSIONS**

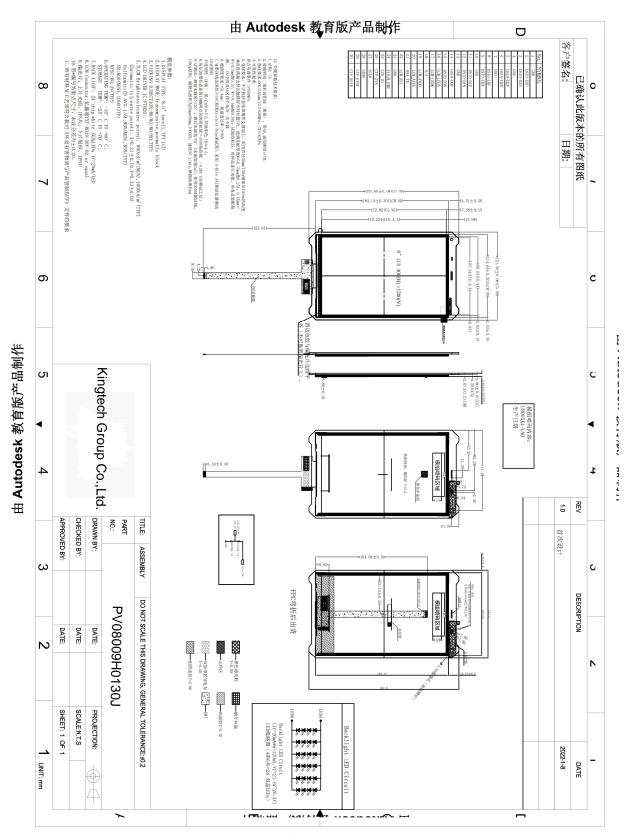


Figure 1.



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# **3** ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	OI CONDITIO		STANDARD VALUE			
	STWBOE	N	MIN	TYP	MAX	T	
Analog operating voltage	AVDD	Ta=+25°C	-0.3	-	6.6	V	
Analog operating voltage	AVEE	Ta=+25°C	-0.3	-	-6.6	V	
Interface Operation Voltage	IOVCC	Ta=+25°C	-0.3	-	4.5	V	
Input Voltage	Vin	Ta=+25°C	-0.3	-	VDD+0.	V	

Table 3.

# NOTE:

- (1). If the module is used above these absolute maximum ratings. It may become permanently damaged. Using the module within the following electrical characteristic conditions are also exceeded, the module will malfunction and cause poor reliability
  - (2). LCM should be grounded during handing LCM.
  - (3). VDD>GND must be maintained.



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# **4** ELECTRICAL CHARACTERISTICS

# 4.1 DC Characteristics

ITEM	SYMBOL	CONDITIONS	STAN	UNIT		
			MIN	TYP	MAX	
Analog operating voltage	AVDD	Ta= +25°C	4.5	5.5	6.5	V
Analog operating voltage	AVEE	Ta=+25°C	-6.5	-5.5	-4.5	V
Interface Operation Voltage	IOVCC1.8	Ta= +25°C	1.65	1.8	1.95	V
Input High Voltage for LCD	VIH	_	0.7Iovc	_	Iovcc	V
Input Low Voltage for LCD	VIL	_	Vss	_	0.3 Iovcc	V
Output High Voltage for LCD	VOH	_	0.8Iovc	_	Iovcc	V
Output Low Voltage for LCD	VOL	_	Vss	_	0.2 Iovcc	V

Table 4.

# 4.2 Back-Light unit

PARAMETER	SYMBOL	REMARK	STANDARD VALUE			UNIT
			MIN	TYP	MAX	
FORWARD VOLTAGE	VF	If=120mA		25.6	-	V
LUMINOUS INTENSITY	Iv	If=120mA		1000		cd/m2
LUMINOUS UNIFORMITY	Iv-m	(min/max)/100	-	80	-	%
CHROMATICITY	X	If -120 A	0.260	-	0.330	
COORDINATES	Y	If=120mA	0.260	-	0.330	
OPERATING TEMPERATURE	-20°C ~ 70°C					
STORAGE TEMPERATURE		-30°C ~ 80°C				

Table 5.

# 4.3 AC Characteristics

Refer to IC data sheet





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# **5** ELECTRO-OPTICAL CHARACTERISTICS

Paramete		Symbol	Condition	Min	Тур	Max	Unit	Remark	
Threshold voltage		Vsat		2.7	3.3	3.3	V	N. 4. 1	
	3			1.2	1.5	1.8	V	Note 1	
	Horizontal	Left(9')		-	80	-	Deg		
Viewing Angle	Horizontai	Right(3')	CR > 10	-	80	-	Deg	Not 2	
range	Vertical	Up(12')	CR > 10	-	80	-	Deg	Not 2	
	vertical	Down(6')		-	80	-	Deg		
Contrast ratio		C/R	Θ = 0°	-	900	-		Not 3	
Transmittance		T(%)	$\Theta = 0_{\circ}$	-	4.1	-		Not 4	
White Ch	White Chromaticity		$\Theta = 0^{\circ}$	-	0.304	-			
white Ch	romaticity	yw	$\Theta = 0$	-	0.337	-			
	Red	xR		-	0.628	-			
	Red	yR		-	0.336	-		Not 5 *Color	
Reproduction	on C	xG	Θ= 0°	-	0.290	-		Filter Glass	
Of color	Green	yG	$\Theta = 0$	-	0.582	-		Glubb	
	Dlas	хB		-	0.140	-			
	Blue	yB		-	0.146	-			
Response Time		Tr+Tf	Θ= 0°		25		smec	Not 6	

Table 6.



#### Note:

1. The definition of Vth & Vsat

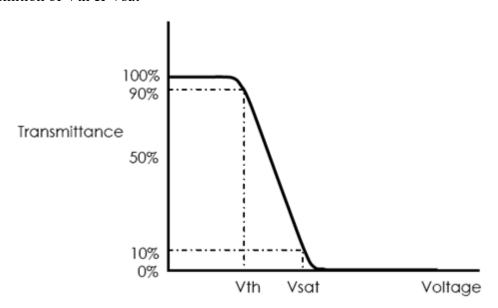


Figure 2. The definition of Vth & Vsat

2. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface.

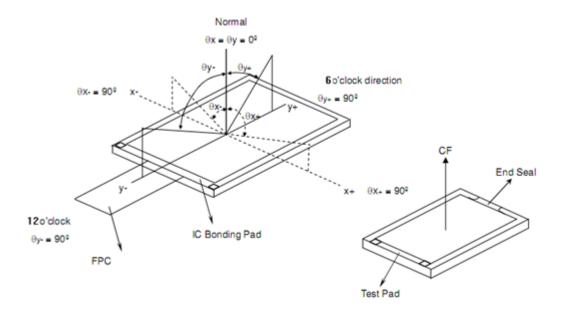


Figure 3.Definition of viewing angle

3. Contrast measurements shall be made at viewing angle of  $\Theta$ = 0° and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state.



# $CR \Box \frac{Luminance when displaying a white raster}{Luminance when displaying a black raster}$

- 4. Transmittance is the value with Polarizer.
- 5. The color chromaticity coordinates specified in Table 6. shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the C/F. Measurement condition is C light source & Halogen Lamp.

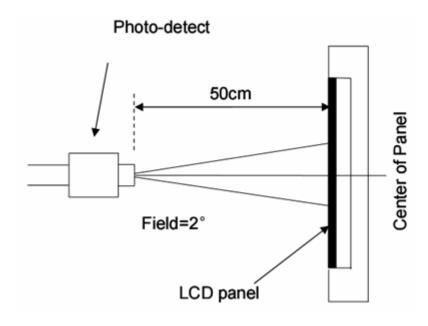


Figure 4 Optical test equipment.

6. The electro-optical response time measurements shall be made as FIGURE 3 shown in Appendix by switching the "data" input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is Tr, and 90% to 10% is Td

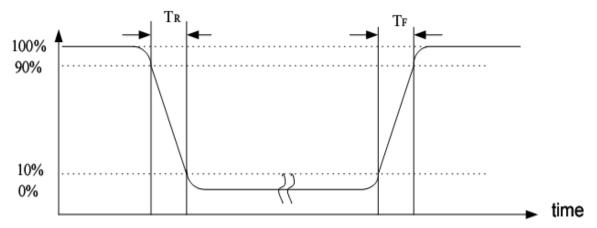


Figure 5.Definition of response time: Tr+Tf



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# **7** INTERFACE PIN CONNECTIONS

1         GND         Ground           2         DSIO-D3P         MIPI-DSI Data differential signal input pins           3         DSIO-D3N         MIPI-DSI Data differential signal input pins           4         GND         Ground           5         DSIO-D2P         MIPI-DSI Data differential signal input pins           6         DSIO-D2N         MIPI-DSI Data differential signal input pins           7         GND         Ground           8         DSIO-CKP         MIPI-DSI Clock differential signal input pins           9         DSIO-CKN         MIPI-DSI Clock differential signal input pins           10         GND         Ground           11         DSIO-DIP         MIPI-DSI Data differential signal input pins           12         DSIO-DIN         MIPI-DSI Data differential signal input pins           13         GND         Ground           14         DSIO-DOP         MIPI-DSI Data differential signal input pins           15         DSIO-DON         MIPI-DSI Data differential signal input pins           16         LCM_LEDA         Backlight positive           17         LCM_LEDA         Backlight positive           18         N/A         No Connection.           19         LCM_AVDD	PIN NO.	SYMBOL	FUNCTION DESCRIPTIONS
3 DSIO-D3N MIPI-DSI Data differential signal input pins 4 GND Ground 5 DSIO-D2P MIPI-DSI Data differential signal input pins 6 DSIO-D2N MIPI-DSI Data differential signal input pins 7 GND Ground 8 DSIO-CKP MIPI-DSI Clock differential signal input pins 9 DSIO-CKN MIPI-DSI Clock differential signal input pins 10 GND Ground 11 DSIO-DIP MIPI-DSI Data differential signal input pins 12 DSIO-DIP MIPI-DSI Data differential signal input pins 13 GND Ground 14 DSIO-DOP MIPI-DSI Data differential signal input pins 15 DSIO-DON MIPI-DSI Data differential signal input pins 16 LCM_LEDA Backlight positive 17 LCM_LEDK Backlight negative 18 N/A No Connection. 19 LCM_AVEE Analog power 20 LCM_AVEE Analog power 21 DSI_TE TE signal 22 LCM_RST Reset signal 23 GND Ground 24 VIO18_PMU Interface Operation Voltage 25 LCD_ID ID signal 26 CTP_2V8(N.C) CTP POWER SUPPLY 27 SCL0 IIC CLOCK 28 SDA0 IIC DATA 29 CTP_EINT INTERUPT SIGNAL	1	GND	Ground
4 GND Ground 5 DSIO-D2P MIPI-DSI Data differential signal input pins 6 DSIO-D2N MIPI-DSI Data differential signal input pins 7 GND Ground 8 DSIO-CKP MIPI-DSI Clock differential signal input pins 9 DSIO-CKN MIPI-DSI Clock differential signal input pins 10 GND Ground 11 DSIO-D1P MIPI-DSI Data differential signal input pins 12 DSIO-D1N MIPI-DSI Data differential signal input pins 13 GND Ground 14 DSIO-D0P MIPI-DSI Data differential signal input pins 15 DSIO-D0N MIPI-DSI Data differential signal input pins 16 LCM_LEDA Backlight positive 17 LCM_LEDK Backlight negative 18 N/A No Connection. 19 LCM_AVDD Analog power 20 LCM_AVEE Analog power 21 DSI_TE TE signal 22 LCM_RST Reset signal 23 GND Ground 24 VIO18_PMU Interface Operation Voltage 25 LCD_ID ID signal 26 CTP_2V8(N.C) CTP POWER SUPPLY 27 SCL0 IIC CLOCK 28 SDA0 IIC DATA 29 CTP_EINT INTERUPT SIGNAL	2	DSIO-D3P	MIPI-DSI Data differential signal input pins
DSIO-D2P MIPI-DSI Data differential signal input pins    DSIO-D2N MIPI-DSI Data differential signal input pins   DSIO-CKP MIPI-DSI Clock differential signal input pins   DSIO-CKN MIPI-DSI Clock differential signal input pins   DSIO-CKN MIPI-DSI Clock differential signal input pins   DSIO-DIP MIPI-DSI Data differential signal input pins   DSIO-DIP MIPI-DSI Data differential signal input pins   DSIO-DIN MIPI-DSI Data differential signal input pins   DSIO-DOP MIPI-DSI Data differential signal input pins   DSIO-DON MIPI-DSI DATA DISTANCE   DSIO-DON MIPI-DSI DATA DISTANC	3	DSIO-D3N	MIPI-DSI Data differential signal input pins
6 DSIO-D2N MIPI-DSI Data differential signal input pins 7 GND Ground 8 DSIO-CKP MIPI-DSI Clock differential signal input pins 9 DSIO-CKN MIPI-DSI Clock differential signal input pins 10 GND Ground 11 DSIO-DIP MIPI-DSI Data differential signal input pins 12 DSIO-DIN MIPI-DSI Data differential signal input pins 13 GND Ground 14 DSIO-DOP MIPI-DSI Data differential signal input pins 15 DSIO-DON MIPI-DSI Data differential signal input pins 16 LCM_LEDA Backlight positive 17 LCM_LEDK Backlight negative 18 N/A No Connection. 19 LCM_AVDD Analog power 20 LCM_AVEE Analog power 21 DSI_TE TE signal 22 LCM_RST Reset signal 23 GND Ground 24 VIO18_PMU Interface Operation Voltage 25 LCD_ID ID signal 26 CTP_2V8(N.C) CTP POWER SUPPLY 27 SCLO IIC CLOCK 28 SDAO IIC DATA	4	GND	Ground
7         GND         Ground           8         DSIO-CKP         MIPI-DSI Clock differential signal input pins           9         DSIO-CKN         MIPI-DSI Clock differential signal input pins           10         GND         Ground           11         DSIO-D1P         MIPI-DSI Data differential signal input pins           12         DSIO-D1N         MIPI-DSI Data differential signal input pins           13         GND         Ground           14         DSIO-D0P         MIPI-DSI Data differential signal input pins           15         DSIO-D0N         MIPI-DSI Data differential signal input pins           16         LCM_LEDA         Backlight positive           17         LCM_LEDK         Backlight negative           18         N/A         No Connection.           19         LCM_AVDD         Analog power           20         LCM_AVEE         Analog power           21         DSI_TE         TE signal           22         LCM_RST         Reset signal           23         GND         Ground           24         VIO18_PMU         Interface Operation Voltage           25         LCD_ID         ID signal           26         CTP_2V8(N.C)         CTP POW	5	DSIO-D2P	MIPI-DSI Data differential signal input pins
B DSIO-CKP MIPI-DSI Clock differential signal input pins  DSIO-CKN MIPI-DSI Clock differential signal input pins  MIPI-DSI Clock differential signal input pins  DSIO-DIP MIPI-DSI Data differential signal input pins  DSIO-DIN MIPI-DSI Data differential signal input pins  MIPI-DSI Data differential signal input pins  DSIO-DOP MIPI-DSI Data differential signal input pins  DSIO-DOP MIPI-DSI Data differential signal input pins  LCM_LEDA Backlight positive  LCM_LEDA Backlight negative  N/A No Connection.  LCM_AVDD Analog power  LCM_AVEE Analog power  LCM_AVEE Analog power  LCM_RST Reset signal  CCM_RST Reset signal  CCM_COUNTY  MIPI-DSI Data differential signal input pins  LCM_LEDK Backlight negative  N/A No Connection.  LCM_LEDK Backlight negative  Interface Operation.  LCM_AVED Analog power  LCM_AVEE Analog power  LCM_AVEE Analog power  LCM_AVEE Analog power  LCM_RST Reset signal  CCM_COUNTY  MIPI-DSI Clock differential signal input pins  LCM_LEDK Backlight positive  LCM_LEDK Backlight negative  N/A No Connection.  LCM_LEDK Backlight negative  Interface Operation.  LCM_AVED Analog power  LCM_AVED Analog power  LCM_AVEE Analog power  LCM_CLCK_LCK_LCK_LCK_LCK_LCK_LCK_LCK_LCK_LC	6	DSIO-D2N	MIPI-DSI Data differential signal input pins
9 DSIO-CKN MIPI-DSI Clock differential signal input pins 10 GND Ground 11 DSIO-DIP MIPI-DSI Data differential signal input pins 12 DSIO-DIN MIPI-DSI Data differential signal input pins 13 GND Ground 14 DSIO-DOP MIPI-DSI Data differential signal input pins 15 DSIO-DON MIPI-DSI Data differential signal input pins 16 LCM_LEDA Backlight positive 17 LCM_LEDK Backlight negative 18 N/A No Connection. 19 LCM_AVDD Analog power 20 LCM_AVEE Analog power 21 DSI_TE TE signal 22 LCM_RST Reset signal 23 GND Ground 24 VIO18_PMU Interface Operation Voltage 25 LCD_ID ID signal 26 CTP_2V8(N.C) CTP POWER SUPPLY 27 SCL0 IIC CLOCK 28 SDA0 IIC DATA 29 CTP_EINT INTERUPT SIGNAL	7	GND	Ground
10         GND         Ground           11         DSIO-D1P         MIPI-DSI Data differential signal input pins           12         DSIO-D1N         MIPI-DSI Data differential signal input pins           13         GND         Ground           14         DSIO-D0P         MIPI-DSI Data differential signal input pins           15         DSIO-D0N         MIPI-DSI Data differential signal input pins           16         LCM_LEDA         Backlight positive           17         LCM_LEDK         Backlight negative           18         N/A         No Connection.           19         LCM_AVDD         Analog power           20         LCM_AVEE         Analog power           21         DSI_TE         TE signal           22         LCM_RST         Reset signal           23         GND         Ground           24         VIO18_PMU         Interface Operation Voltage           25         LCD_ID         ID signal           26         CTP_2V8(N.C)         CTP POWER SUPPLY           27         SCL0         IIC CLOCK           28         SDA0         IIC DATA           29         CTP_EINT         INTERUPT SIGNAL	8	DSIO-CKP	MIPI-DSI Clock differential signal input pins
11 DSIO-D1P MIPI-DSI Data differential signal input pins 12 DSIO-D1N MIPI-DSI Data differential signal input pins 13 GND Ground 14 DSIO-D0P MIPI-DSI Data differential signal input pins 15 DSIO-D0N MIPI-DSI Data differential signal input pins 16 LCM_LEDA Backlight positive 17 LCM_LEDK Backlight negative 18 N/A No Connection. 19 LCM_AVDD Analog power 20 LCM_AVEE Analog power 21 DSI_TE TE signal 22 LCM_RST Reset signal 23 GND Ground 24 VIO18_PMU Interface Operation Voltage 25 LCD_ID ID signal 26 CTP_2V8(N.C) CTP POWER SUPPLY 27 SCL0 IIC CLOCK 28 SDA0 IIC DATA 29 CTP_EINT INTERUPT SIGNAL	9	DSIO-CKN	MIPI-DSI Clock differential signal input pins
12 DSIO-D1N MIPI-DSI Data differential signal input pins 13 GND Ground 14 DSIO-D0P MIPI-DSI Data differential signal input pins 15 DSIO-D0N MIPI-DSI Data differential signal input pins 16 LCM_LEDA Backlight positive 17 LCM_LEDK Backlight negative 18 N/A No Connection. 19 LCM_AVDD Analog power 20 LCM_AVEE Analog power 21 DSI_TE TE signal 22 LCM_RST Reset signal 23 GND Ground 24 VIO18_PMU Interface Operation Voltage 25 LCD_ID ID signal 26 CTP_2V8(N.C) CTP POWER SUPPLY 27 SCL0 IIC CLOCK 28 SDA0 IIC DATA 29 CTP_EINT INTERUPT SIGNAL	10	GND	Ground
13         GND         Ground           14         DSIO-D0P         MIPI-DSI Data differential signal input pins           15         DSIO-D0N         MIPI-DSI Data differential signal input pins           16         LCM_LEDA         Backlight positive           17         LCM_LEDK         Backlight negative           18         N/A         No Connection.           19         LCM_AVDD         Analog power           20         LCM_AVEE         Analog power           21         DSI_TE         TE signal           22         LCM_RST         Reset signal           23         GND         Ground           24         VIO18_PMU         Interface Operation Voltage           25         LCD_ID         ID signal           26         CTP_2V8(N.C)         CTP POWER SUPPLY           27         SCL0         IIC CLOCK           28         SDA0         IIC DATA           29         CTP_EINT         INTERUPT SIGNAL	11	DSIO-D1P	MIPI-DSI Data differential signal input pins
14 DSIO-DOP MIPI-DSI Data differential signal input pins 15 DSIO-DON MIPI-DSI Data differential signal input pins 16 LCM_LEDA Backlight positive 17 LCM_LEDK Backlight negative 18 N/A No Connection. 19 LCM_AVDD Analog power 20 LCM_AVEE Analog power 21 DSI_TE TE signal 22 LCM_RST Reset signal 23 GND Ground 24 VIO18_PMU Interface Operation Voltage 25 LCD_ID ID signal 26 CTP_2V8(N.C) CTP POWER SUPPLY 27 SCL0 IIC CLOCK 28 SDA0 IIC DATA 29 CTP_EINT INTERUPT SIGNAL	12	DSIO-D1N	MIPI-DSI Data differential signal input pins
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16         LCM_LEDA         Backlight positive           17         LCM_LEDK         Backlight negative           18         N/A         No Connection.           19         LCM_AVDD         Analog power           20         LCM_AVEE         Analog power           21         DSI_TE         TE signal           22         LCM_RST         Reset signal           23         GND         Ground           24         VIO18_PMU         Interface Operation Voltage           25         LCD_ID         ID signal           26         CTP_2V8(N.C)         CTP POWER SUPPLY           27         SCL0         IIC CLOCK           28         SDA0         IIC DATA           29         CTP_EINT         INTERUPT SIGNAL	14	DSIO-D0P	MIPI-DSI Data differential signal input pins
17         LCM_LEDK         Backlight negative           18         N/A         No Connection.           19         LCM_AVDD         Analog power           20         LCM_AVEE         Analog power           21         DSI_TE         TE signal           22         LCM_RST         Reset signal           23         GND         Ground           24         VIO18_PMU         Interface Operation Voltage           25         LCD_ID         ID signal           26         CTP_2V8(N.C)         CTP POWER SUPPLY           27         SCL0         IIC CLOCK           28         SDA0         IIC DATA           29         CTP_EINT         INTERUPT SIGNAL	15	DSIO-D0N	MIPI-DSI Data differential signal input pins
18         N/A         No Connection.           19         LCM_AVDD         Analog power           20         LCM_AVEE         Analog power           21         DSI_TE         TE signal           22         LCM_RST         Reset signal           23         GND         Ground           24         VIO18_PMU         Interface Operation Voltage           25         LCD_ID         ID signal           26         CTP_2V8(N.C)         CTP POWER SUPPLY           27         SCL0         IIC CLOCK           28         SDA0         IIC DATA           29         CTP_EINT         INTERUPT SIGNAL	16	LCM_LEDA	Backlight positive
19         LCM_AVDD         Analog power           20         LCM_AVEE         Analog power           21         DSI_TE         TE signal           22         LCM_RST         Reset signal           23         GND         Ground           24         VIO18_PMU         Interface Operation Voltage           25         LCD_ID         ID signal           26         CTP_2V8(N.C)         CTP POWER SUPPLY           27         SCL0         IIC CLOCK           28         SDA0         IIC DATA           29         CTP_EINT         INTERUPT SIGNAL	17	LCM_LEDK	Backlight negative
20         LCM_AVEE         Analog power           21         DSI_TE         TE signal           22         LCM_RST         Reset signal           23         GND         Ground           24         VIO18_PMU         Interface Operation Voltage           25         LCD_ID         ID signal           26         CTP_2V8(N.C)         CTP POWER SUPPLY           27         SCL0         IIC CLOCK           28         SDA0         IIC DATA           29         CTP_EINT         INTERUPT SIGNAL	18	N/A	No Connection.
21         DSI_TE         TE signal           22         LCM_RST         Reset signal           23         GND         Ground           24         VIO18_PMU         Interface Operation Voltage           25         LCD_ID         ID signal           26         CTP_2V8(N.C)         CTP POWER SUPPLY           27         SCL0         IIC CLOCK           28         SDA0         IIC DATA           29         CTP_EINT         INTERUPT SIGNAL	19	LCM_AVDD	Analog power
22         LCM_RST         Reset signal           23         GND         Ground           24         VIO18_PMU         Interface Operation Voltage           25         LCD_ID         ID signal           26         CTP_2V8(N.C)         CTP POWER SUPPLY           27         SCL0         IIC CLOCK           28         SDA0         IIC DATA           29         CTP_EINT         INTERUPT SIGNAL	20	LCM_AVEE	Analog power
23         GND         Ground           24         VIO18_PMU         Interface Operation Voltage           25         LCD_ID         ID signal           26         CTP_2V8(N.C)         CTP POWER SUPPLY           27         SCL0         IIC CLOCK           28         SDA0         IIC DATA           29         CTP_EINT         INTERUPT SIGNAL	21	DSI_TE	TE signal
24       VIO18_PMU       Interface Operation Voltage         25       LCD_ID       ID signal         26       CTP_2V8(N.C)       CTP POWER SUPPLY         27       SCL0       IIC CLOCK         28       SDA0       IIC DATA         29       CTP_EINT       INTERUPT SIGNAL	22	LCM_RST	Reset signal
25         LCD_ID         ID signal           26         CTP_2V8(N.C)         CTP POWER SUPPLY           27         SCL0         IIC CLOCK           28         SDA0         IIC DATA           29         CTP_EINT         INTERUPT SIGNAL	23	GND	Ground
26         CTP_2V8(N.C)         CTP POWER SUPPLY           27         SCL0         IIC CLOCK           28         SDA0         IIC DATA           29         CTP_EINT         INTERUPT SIGNAL	24	VIO18_PMU	Interface Operation Voltage
27         SCL0         IIC CLOCK           28         SDA0         IIC DATA           29         CTP_EINT         INTERUPT SIGNAL	25	LCD_ID	ID signal
28 SDA0 IIC DATA 29 CTP_EINT INTERUPT SIGNAL	26	CTP_2V8(N.C)	CTP POWER SUPPLY
29 CTP_EINT INTERUPT SIGNAL	27	SCL0	IIC CLOCK
	28	SDA0	IIC DATA
30 CTP_RSTB TP RESET SIGNAL	29	CTP_EINT	INTERUPT SIGNAL
	30	CTP_RSTB	TP RESET SIGNAL

Table 7.



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# 8 INITIALIZED CODE

Please contact us for details.

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# 9 SPECIFICATION OF QUALITY ASSURANCE

# 9.1 Summary

The customer should check and accept the products of Kingtech within one month after reception. This standard for Quality Assurance should affirm the quality of LCD products to supply to purchaser by Kingtech Group Co.,Ltd. Entire process is controlled according to QS9000.

# 9.2 Standard for quality test

(1) Inspection

Before delivering, the supplier should take the following tests, and affirm the quality of product.

(2) Electro-Optical Characteristics

According to the individual specification to test the product.

(3) Test of Appearance Characteristics:

According to the individual specification to test the product.

(4) Test of Reliability Characteristics

According to the definition of reliability on specification for test product.

(5) Delivery Test

Before delivering, the supplier should take the delivery test

(6) Sampling Method: GB/T2828.1-2003, Level II

(7) The defects classify of AQL as following

Major defect: AQL=0.65

Minor defect: AQL=1.5

#### 9.3 Nonconforming Analysis & Deal With Manners

☆Nonconforming Analysis

- (1) Purchaser should supply the detail data of nonconforming sample and the non-suitable state.
- (2) After accepting the detail data from purchaser ,the analysis of nonconforming should be finished in two weeks.
  - (3) If supplier can not finish analysis on time ,must announce purchaser before two weeks.
  - ☆Disposition of nonconforming
- (1) If find any supplier defect during assembly line, supplier must change the good product for every defect after recognition.
- (2) Both supplier and customer should analysis the reason and discuss the disposition of nonconforming when the reason of nonconforming is not sure.

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#### 9.4 Agreement items.

Both sides should discuss together when the following problems happen:

- (1) There is any problem of standard of quality assurance, and both sides think that must be modifier.
  - (2) There is any argument item which does not record in the quality assurance.
  - (3) Any other special problem.

#### 9.5 Standard of the Product Appearance Test

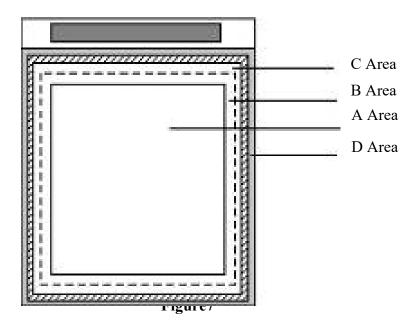
- 9.5.1 Manner of appearance test
- (1) The test must be under 20W\*2 or 40W fluorescent light ,and the distance of view must be at 30±5 cm.
  - (2) When test the model of Transmissive product must add the reflective plate.
  - (3) Definition of Area:

A Area: Active area

B Area: Viewing area

C Area: Out of viewing area

D Area: Seal area



# 9.5.2 Basic principle:

(1) It will accord to the AQL when the standard can not be described.







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- (2) The sample of the lowest acceptable quality level must be discussed by both supplier and customer when any dispute happened.
- (3) Must add new item on time when it is necessary.

# 9.6 Inspection specification

NO	Item	Criterion	AQL
01	Electrical Testing	<ul> <li>1.1 Missing vertical, horizontal segment, segment contrast defect.</li> <li>1.2 Missing character, dot or icon.</li> <li>1.3 Display malfunction.</li> <li>1.4 No function or no display.</li> <li>1.5 Current consumption exceeds product specifications.</li> <li>1.6 LCD viewing angle defect.</li> <li>1.7 Contrast defect</li> </ul>	0.65



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02	LCD black spots, white spots, color spots, contamination, scratches (display/non-display)	Length L≤2.5  L≤1.5	$ \begin{array}{c c}  & & & & \\  & & & & \\  & & & & \\ \hline  & & & \\ \hline  & & & & \\ \hline  $	s follo	V.A Ignore  3  2  0 5  wing dra  Acceptab A.A Ignore 2	No mothan to spots within 5mm  Sile QTY V.A Ignore 3	Remark  No more than two lines within 5mm	m	1.5
03	Polarizer bubbles Ignore	If bubbles easy to fin  Size $\phi \leq 0.15$ $0.15 < \phi \leq$	d, must ch		n specify Accepta			ation, not	1.5



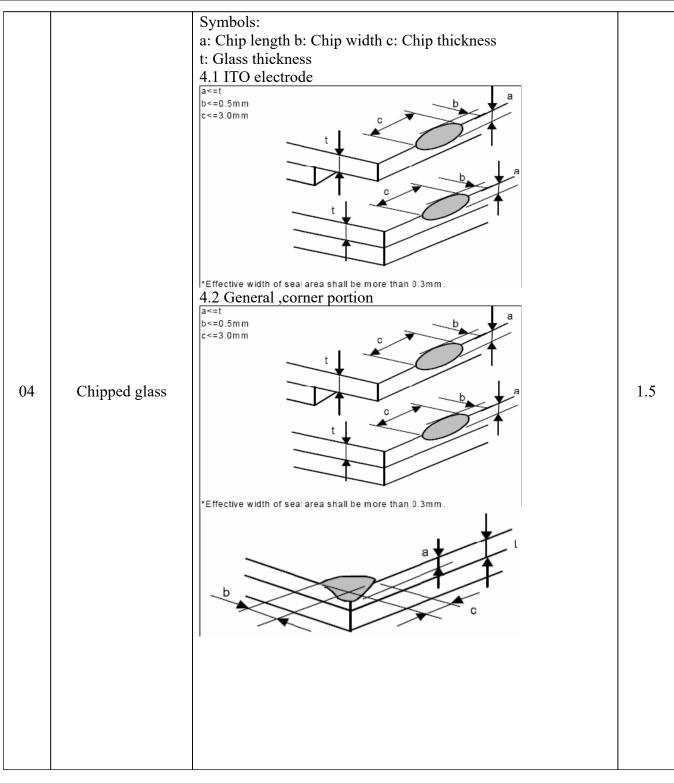
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05	Cracked glass	The LCD with extensive crack is not acceptable.	0.65
		6.1 Illumination source flickers when lit.	0.65
		6.2 Spots or scratches that appear when lit must be judged	1.5
06 Backlight elements		using LCD spot, lines and contamination standards.	
		6.3 Backlight doesn't light or color is wrong	
			0.65



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		7.1 No unmelted solder paste may be present on the PCB.	1.5
07		7.2 No cold solder joints, missing solder connections, oxidation	
	0.11	or icicle.	1.5
	Soldering	7.3 No residue or solder balls on PCB.	
		7.4 No short circuits in components on PCB.	1.5
			0.65
		8.1 No oxidation, contamination, curves or, bends on interface	1.5
		pin (OLB) of TCP.	
		8.2 No cracks on interface pin(OLB) of TCP	0.65
		8.3 NO contamination, solder residue or solder balls on	1.5
		product.	
		8.4 The IC on the TCP may not be damaged, circuits.	0.65
		8.5 The residual rosin or tin oil of soldering (component or chip	1.5
		component) is not burned into brown or black color. 8.6	
		Sealant on top of the ITO circuit has not hardened	1.5
08	General appearance	8.7 Pin type must match type in specification sheet.	0.65
	General appearance	8.8 LCD pin loose or missing pins.	0.65
		8.9 Product packaging must the same as specified on packaging	0.65
		specification sheet.	
		8.10 Product dimension and structure must conform to product	0.65
		specification sheet.	

Table 8.



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# **10 RELIABILITY**

NO	Test Item	Description	Test Condition	
1	High temperature storage	Endurance test applying the high storage temperature for a long time	70°С,24 Н	
2	Low temperature storage	Endurance test applying the low storage temperature for a long time	-25°C,24H	
3	High temperature operation	Endurance test applying the electric stress under high temperature for a long time	60°C,24H	
4	Low temperature operation	Endurance test applying the electric stress under low temperature for a long time	-10°C,24H	
5	High temperature /humidity storage	Endurance test applying the high temperature and high humidity storage for a long time	50°С,90% R.H 24H	
6	High temperature /humidity operation	Endurance test applying electric stress under high temperature and high humidity for a long time	40°C 90% R.H 24H	
7	Temperature Cycle	Endurance test applying the low and high temperature cycle -10°C → 25°C → 60°C →25°C 30min 5min 30min 5min one cycle	-10°C/60°C 10 cycles	
8	Vibration test	Endurance test applying the vibration during transportation and using	10Hz~50Hz Swing:0.75mm time:30min	
9	Fall test	Endurance test dropping the LCM from a high place	600mm height	
10	Static electricity test	Endurance test applying static electric stress to terminal	Contact discharge: ±2KV~4KV Air discharge: ±2KV~8KV	

# Table 9.

# NOTE: TEST CONDITION

- (1) Temperature and humidity: If no specification, temp. set at 25±2°C, humidity set at 60±5%RH.
- (2) Operating state: Samples subject to the test shall be in "operating" condition.

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# 11 USING LCD MODULES

#### 11.1 LIQUID CRYSTAL DISPLAY MODULES

LCD is composed of glass and polarizer. Pay attention to the following items when handling.

- (1) Please keep the temperature within specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.
- (2) Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.).
- (3) N-hexane is recommended for cleaning the adhesives used to attach front/rear polarizers and reflectors made of organic substances which will be damaged by chemicals such as acetone, toluene, ethanol and isopropylalcohol.
- (4) When the display surface becomes dusty, wipe gently with absorbent cotton or other soft material like chamois soaked in petroleum benzin. Do not scrub hard to avoid damaging the display surface.
- (5) Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading.
- (6) Avoid contacting oil and fats.
- (7) Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizers. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.
- (8) Do not put or attach anything on the display area to avoid leaving marks on.
- (9) Do not touch the display with bare hands. This will stain the display area and degradate insulation between terminals (some cosmetics are determinated to the polarizers).
- (10) As glass is fragile. It tends to become or chipped during handling especially on the edges. Please avoid dropping or rising.

#### 11.2 PRECAUTION FOR HANDING LCD MODULES

Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.

- (1) Do not alter, modify or change the shape of the tab on the metal frame.
- (2) Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
- (3) Do not damage or modify the pattern writing on the printed circuit board.
- (4) Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
- (5) Except for soldering the interface, do not make any alterations or modifications with a soldering



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iron.

- (6) Do not drop, bend or twist LCM.
- (7) In order to avoid the cracking of the FPC, you should to pay attention to the area of FPC(R50mm) where the FPC was bent .the edge of coverlay; the area of surface of Ni-Au plating, the area of soldering land, the area of through hole.

#### 11.3 ELECTRO-STATIC DISCHARGE CONTROL

Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC.

- (1) Make certain that you are grounded when handing LCM.
- (2) Before remove LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential.
- (3) When soldering the terminal of LCM, make certain the AC power source for the soldering iron does not leak.
- (4) When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.
- (5) As far as possible make the electric potential of your work clothes and that of the work bench the ground potential.
- (6) To reduce the generation of static electricity be careful that the air in the work is not too dried. A relative humidity of 0%-60% is recommended.

# 11.4 PRECAUTIONS FOR OPERATION

- (1) Viewing angle varies with the change of liquid crystal driving voltage (VO). Adjust VO to show the best contrast.
- (2) Driving the LCD in the voltage above the limit shortens its life.
- (3) Response time is greatly delayed at temperature below the operating temperature range. However, this does not mean the LCD will be out of the order. It will recover when it returns to the specified temperature range.
- (4) If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then back on.
- (5) Condensation on terminals can cause an electrochemical reaction disrupting the terminal circuit. Therefore, it must be used under the relative condition of  $40^{\circ}$ C, 50% RH.
- (6) When turning the power on, input each signal after the positive/negative voltage becomes stable.



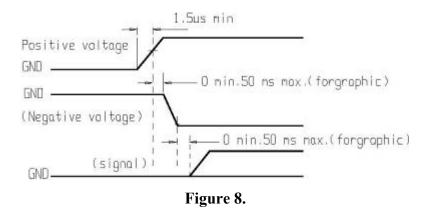
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#### 11.5 STORAGE

When storing LCDs as spares for some years, the following precaution are necessary.

- (1) Store them in a sealed polyethylene bag. If properly sealed, there is no need for dessicant.
- (2) Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C.
- (3) The polarizer surface should not come in contact with any other objects. (We advise you to store them in the container in which they were shipped.)
- (4) Environmental conditions:
- Do not leave them for more than 160hrs. at 70°C.
- Should not be left for more than 48hrs. at -20°C.

#### 11.6 SAFETY

- (1) It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- (2) If any liquid leakes out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and ater.

# 11.7 LIMITED WARRANTY

Unless agreed between Kingtech and customer, Kingtech will replace or repair any of its LCD modules which are found to be functionally defective when inspected in accordance with Kingtech LCD modules acceptance standards (copies available upon request) for a period of one year from date of shipments. Cosmetic/visual defects must be returned to Kingtech within 90 days of shipment. Confirmation of such date shall be based on freight documents. The warranty liability of Kingtech limited to repair and/or replacement on the terms set forth above. Kingtech will not be responsible for any subsequent or consequential events.



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#### 11.8 RETURN LCM UNDER WARRANTY

No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are :

- Broken LCD glass.
- Circuit modified in any way, including addition of components.

  Module repairs will be invoiced to the customer upon mutual agreement. Modules must be returned with sufficient description of the failures or defects. Any connectors or cable installed by the customer must be removed completely without damaging the PCB's eyelet, conductors and terminals.

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# 12. 包装方式 (PACKING MODE) 仅供参考详情下单后再定

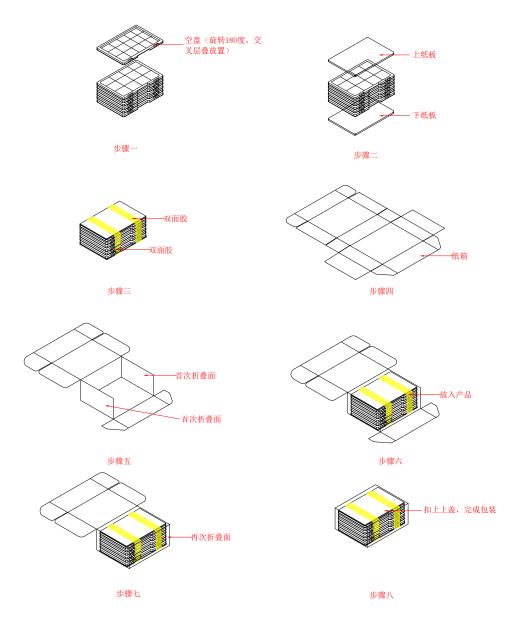
#### 1 包装规范

- 1.1 装箱办法见《包装方法示意图》。装箱数量则由每一个吸塑盘所装的数量决定。每箱装一叠吸塑,每叠 11 个吸塑盘,上面一个吸塑盘不装产品,交叉层叠放置,上下需用纸板、胶纸固定。
- 1.2 小箱: 材料为 K3K, 外腔尺寸为 485\*355\*130mm。
- 1.3 吸塑盘: PET 透防静电或着 PS 黑防静电材料,厚度为 0.6MIN,外形尺寸(根据背光供应商提供 吸塑盘尺寸为准),用量为每箱 11Pcs。
- 1.4 纸板:材料为 A-A 瓦楞纸板,用量为每箱 2Pcs。
- 1.5 装箱数量的计算:

每个吸塑盒所装数量\*10(层)

每个吸塑盘装产品数量 10, 每箱的装箱数量为: 10\*10=100 (Pcs) 产品.

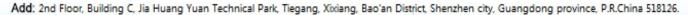
小箱包装示意图:



# 2 按产品型号及物料编号贴上标签及盖上 PASS 章

正常情况下,使用统一的标签。部分产品有写上客户的型号或物料编号按"产品型号及客户

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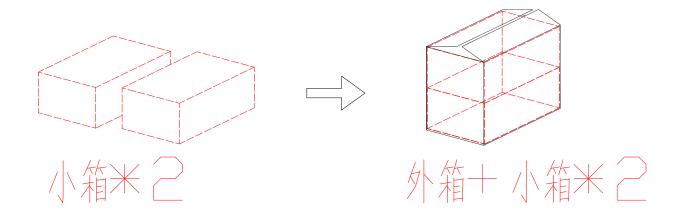
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物料对应表"进行填写。特殊专用的产品标签及出港或走快递的产品,包装标签按跟单人员提供的进行标明。

# 3 出港及走快递的产品,需要外箱

出港及走快递的产品建议用大小箱包装,即两个小箱加外箱。(如图)



NO.	Item	Dimensions	Quantity	Remark
1	TRAY	One tray	10	
2	SMALL CARTON	One carton/11 tray	100	
3	LARGE CARTON	One carton/2 small carton	200	

# 4 打包作业流程图

